FPGA Master Class in SEEE using Open Source Hardware and Software

On 1 November 2019, the INTEL IOT Laboratory in the School of Electrical and Electronic Engineering hosted Goran Mahovlić, the technical coordinator of the Radiona Makerspace in Zagreb in Croatia. For the past 3 years Radiona have been involved in a highly-regarded project to develop open source Field Programmable Gate Array (FPGA) development boards. Goran is centrally involved in the project and he visited TU Dublin to give a masterclass on using the Radiona FPGA platform. FPGAs are increasingly becoming a mainstream technology as they offer inexpensive power economy combined with high computing speeds.

Goran’s practical workshop showed how the Radiona FPGA platform which is intended to map to the technologies of the main FPGA vendors, could be used for a wide variety of roles, including communications, driving displays, audio processing and retro gaming.

The event which was facilitated through the RAP-A INSPIRE programme and was organised by SEEE colleague Frank Duignan. The workshop was attended by 10 academic colleagues and researchers from across the City, Blanchardstown and Tallaght campuses of TU Dublin.

First ESTeEM Lunch of New Academic Year

The first ESTeEM lunch of the new academic year was held in the Gleeson Hall, TU Dublin - City Campus, Kevin St on Thursday 17 October 2019. ESTeEM is a unique mentorship programme for female students in Engineering and Computing to address gender equality. The purpose is to bring students, ranging from females in electrical apprenticeship up through post graduates, together with women role models who are actively working in Amazon, Arup, Dublin City Council, ESB, MasterCard, SAP and Schneider Electric. Training for mentors and TU Dublin staff is provided by Engineers Ireland. The EsTeEM project was developed and is managed by SEEE colleague, Leslie Shoemaker.